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Fig. 1

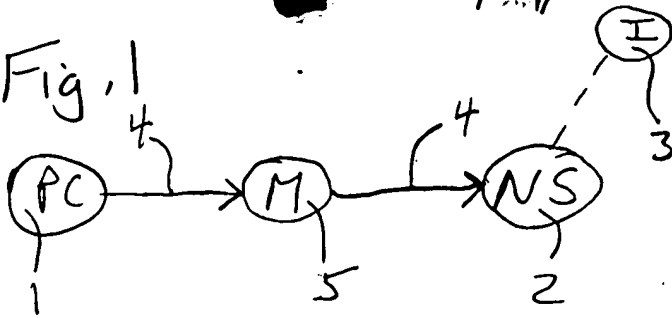


Fig. 2

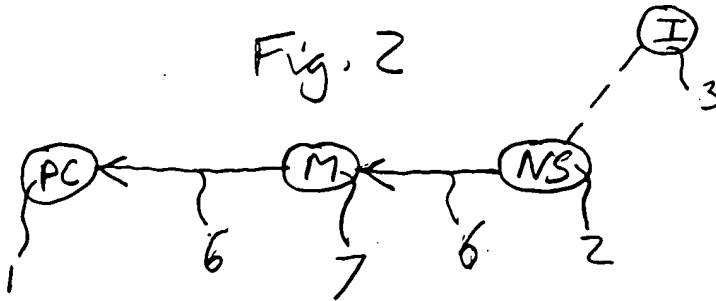


Fig. 3

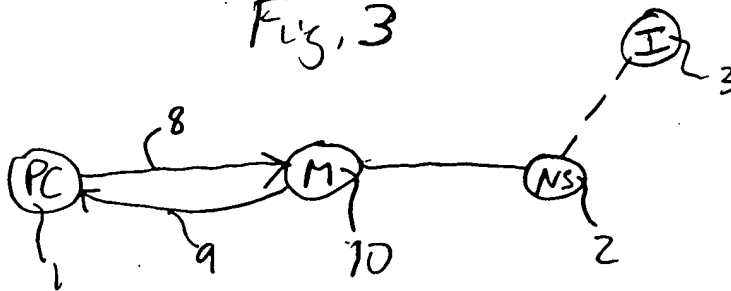


Fig. 4A

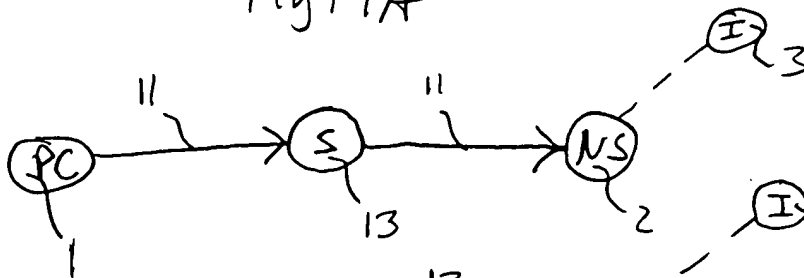


Fig. 4B

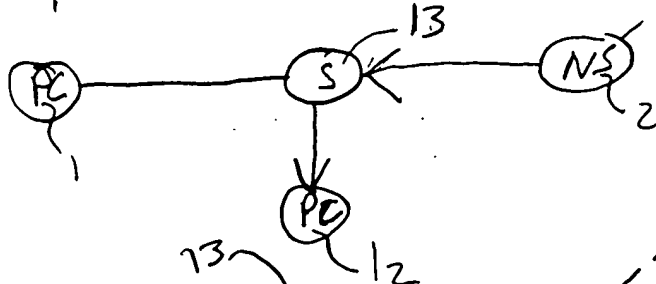
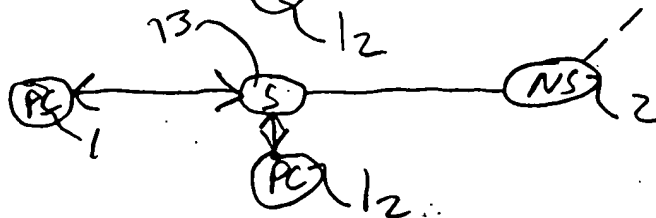


Fig. 4C



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Fig. 5A

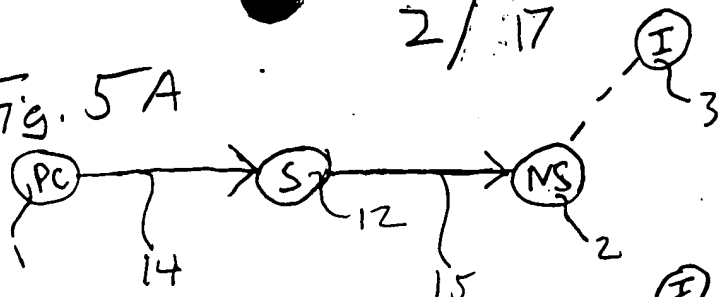


Fig. 5B

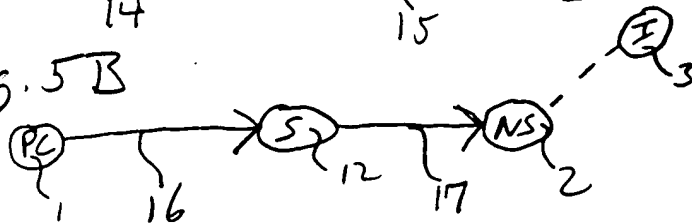


Fig. 6

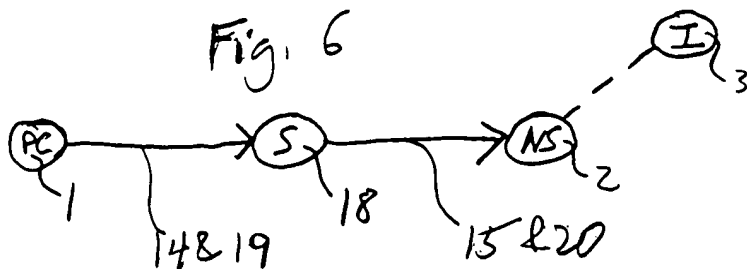


Fig. 7

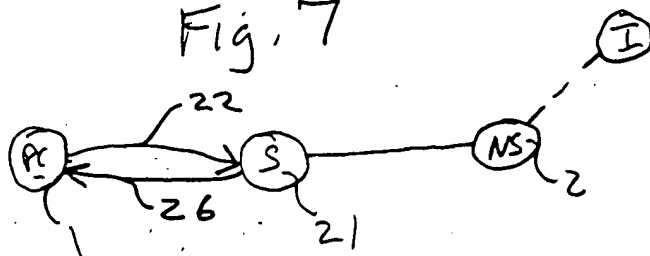
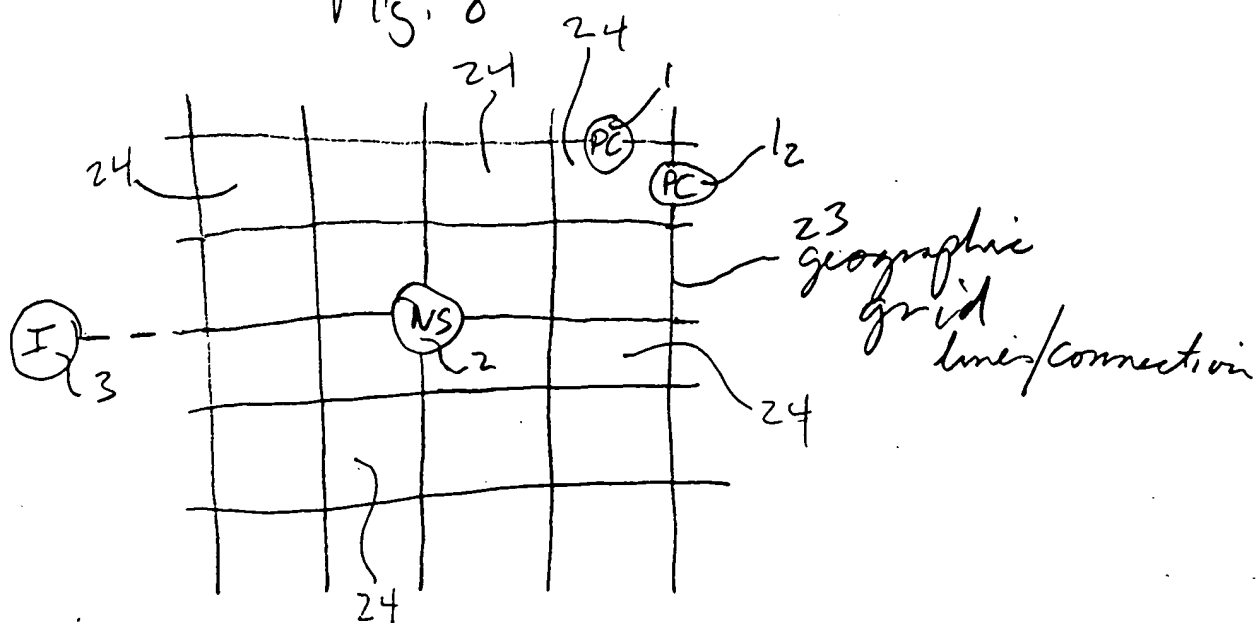


Fig. 8



30

M

50

40

1 PC

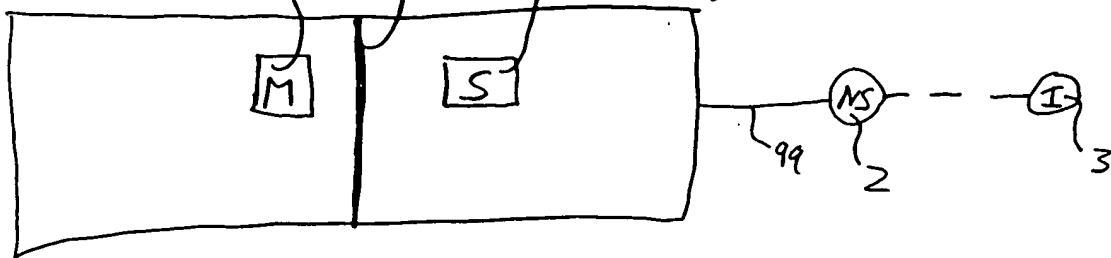


Fig. 10A

1 PC

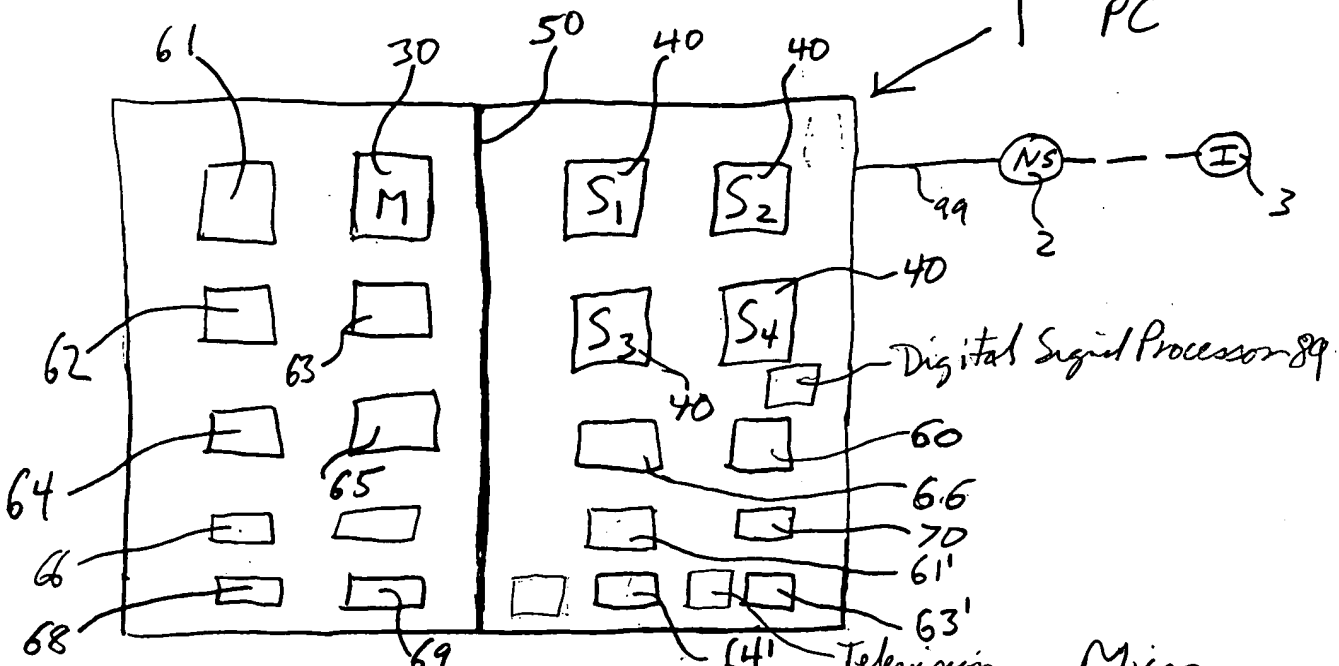


Fig. 10 C

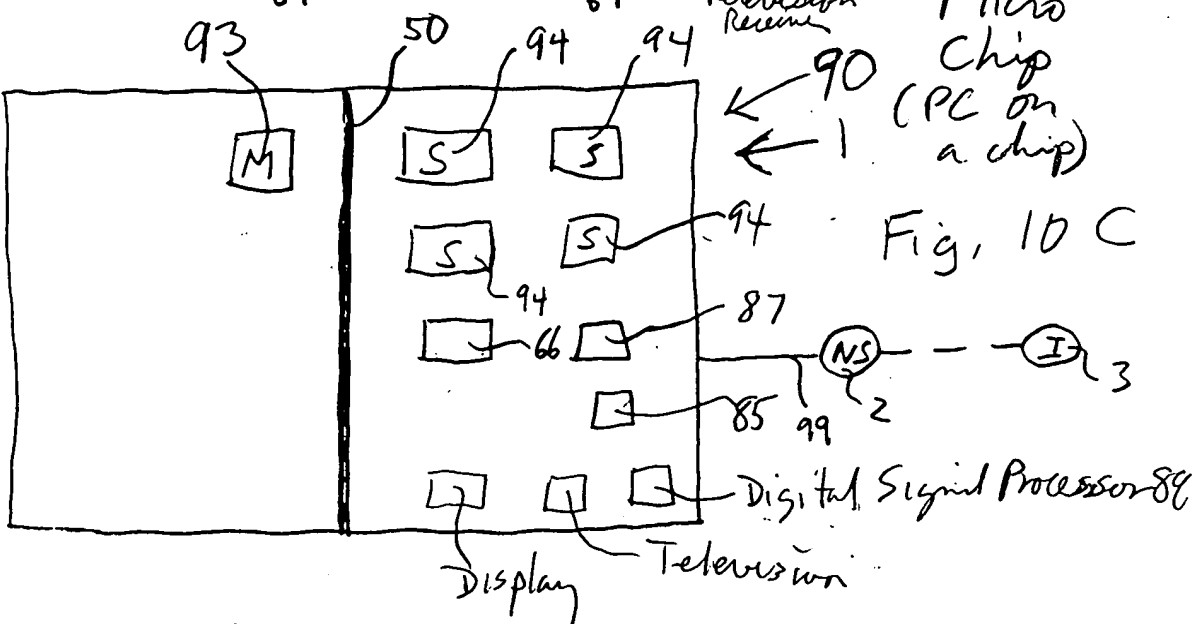


Fig. 9

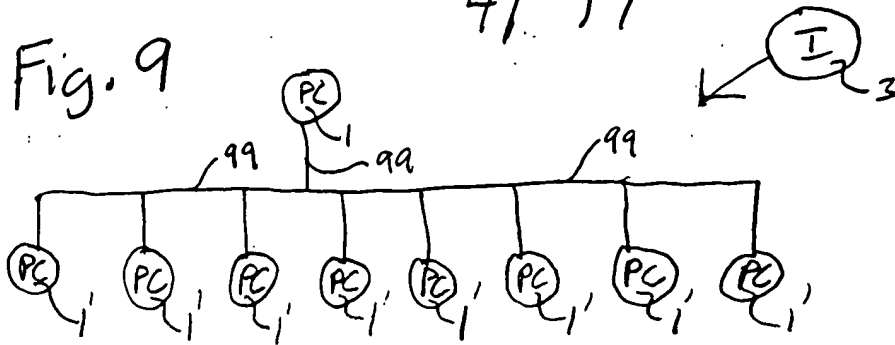


Fig. 10D

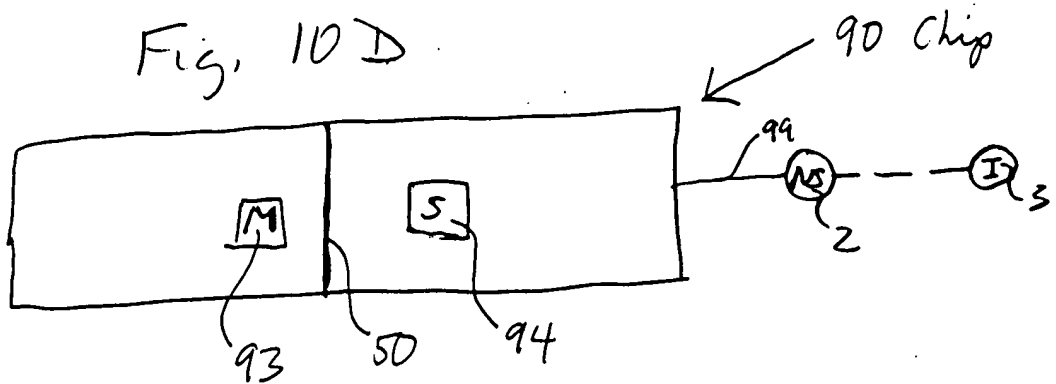


Fig. 11

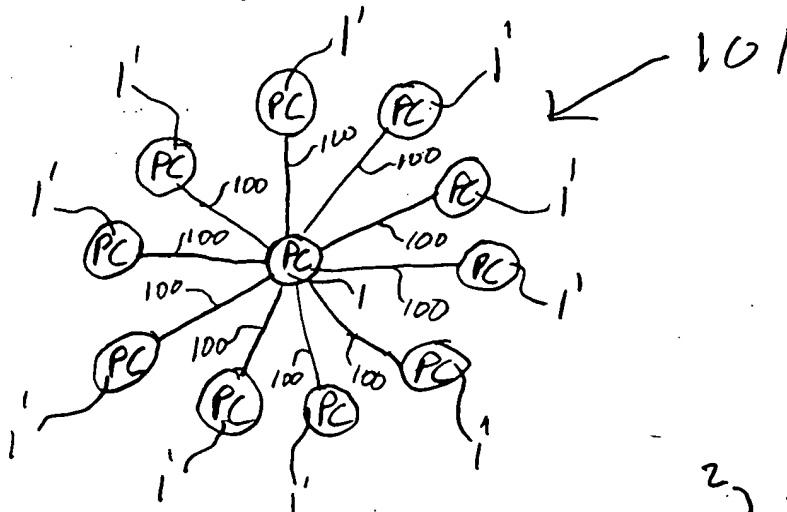
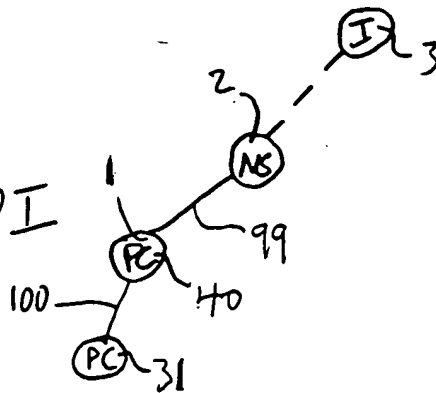


Fig. 10I



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Fig. 10E

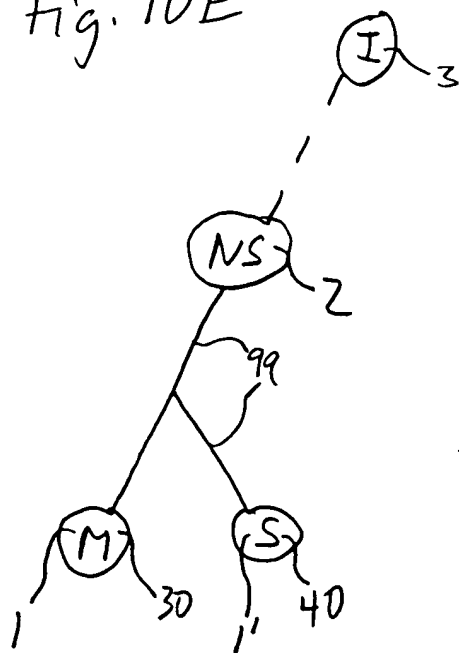


Fig. 10F

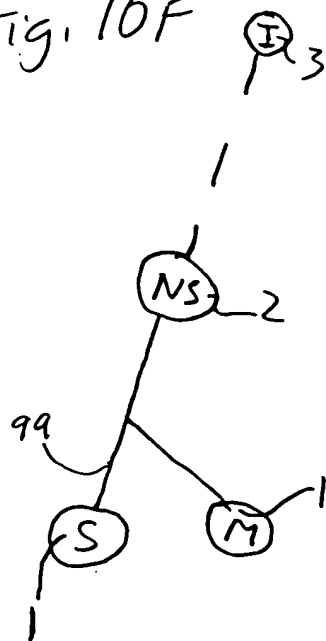


Fig. 10G

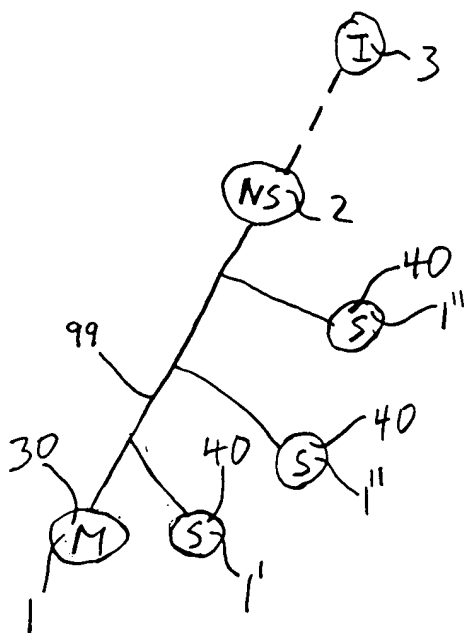
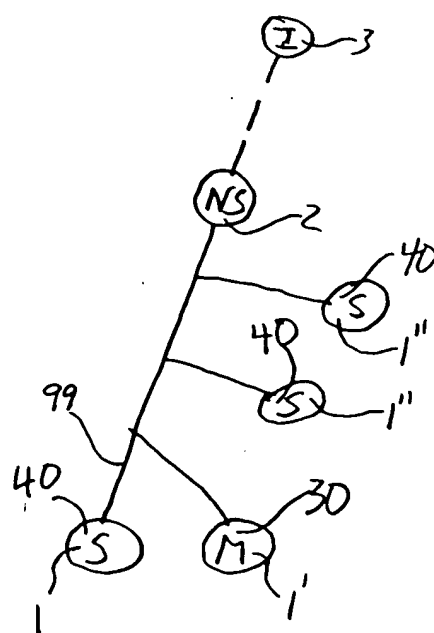


Fig. 10H



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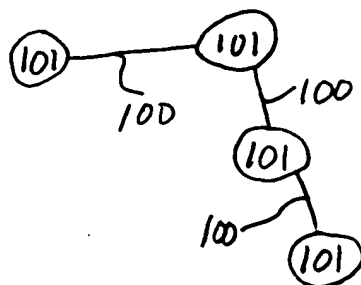
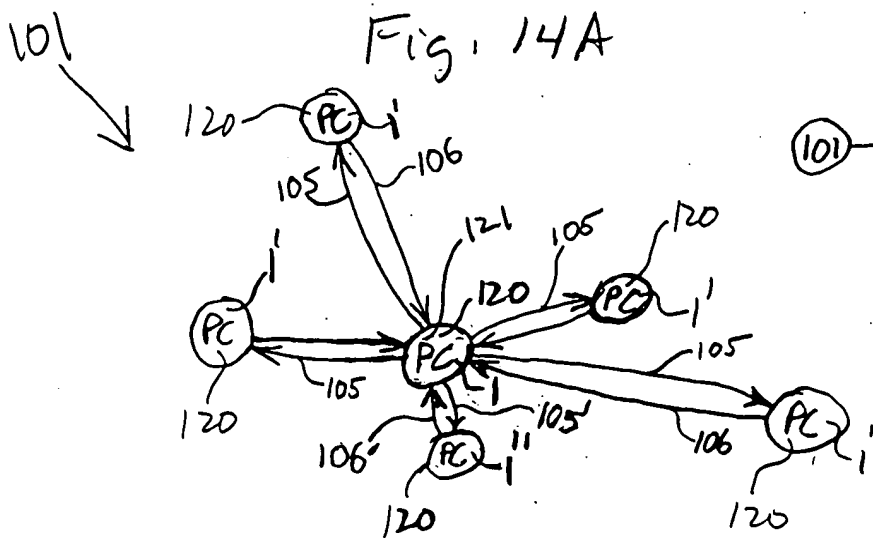
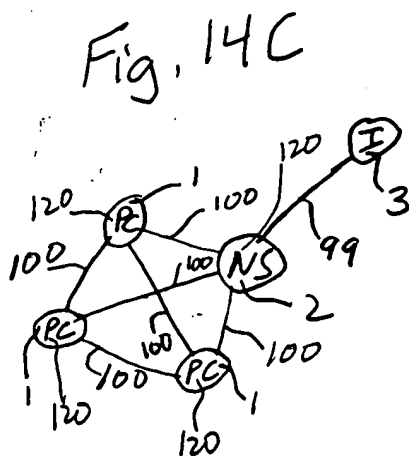
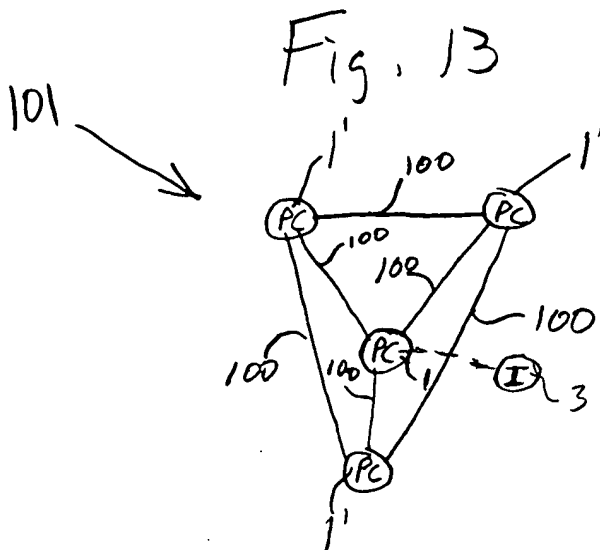
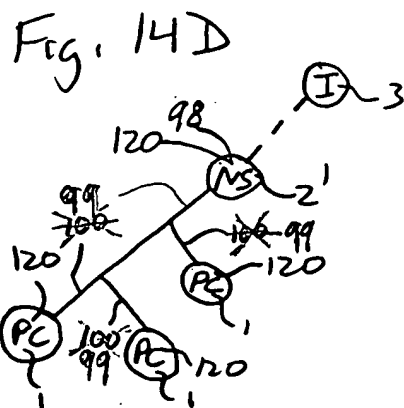
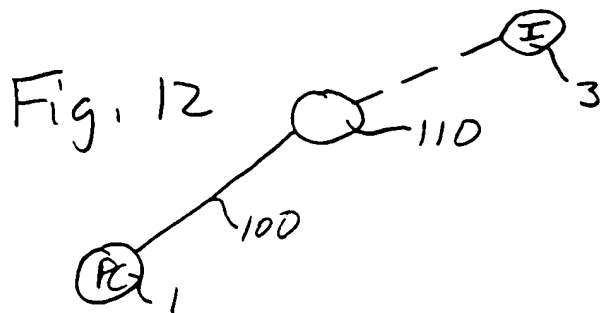
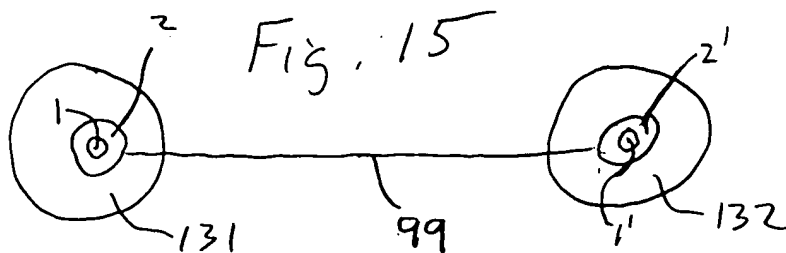


Fig. 14B



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Fig. 10K

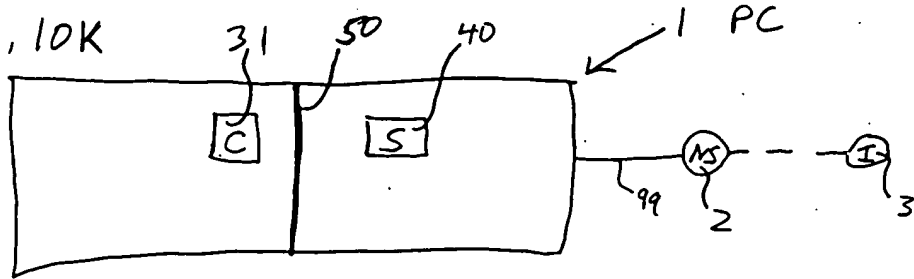


Fig. 10J

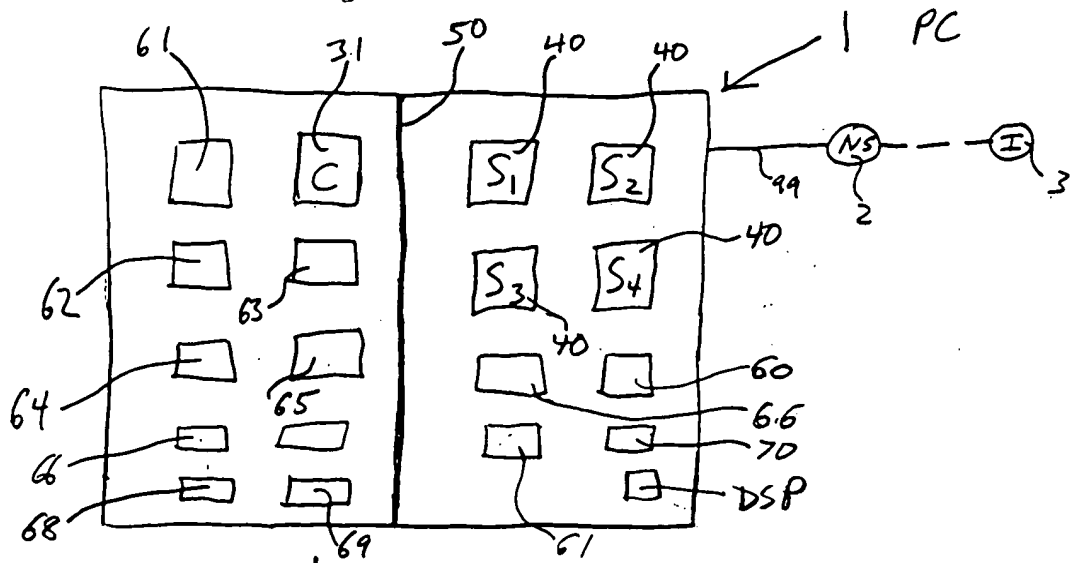


Fig. 10L

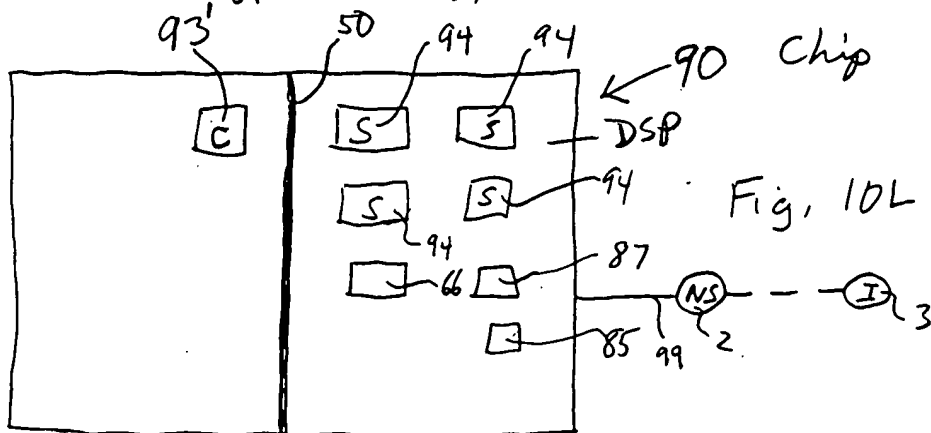
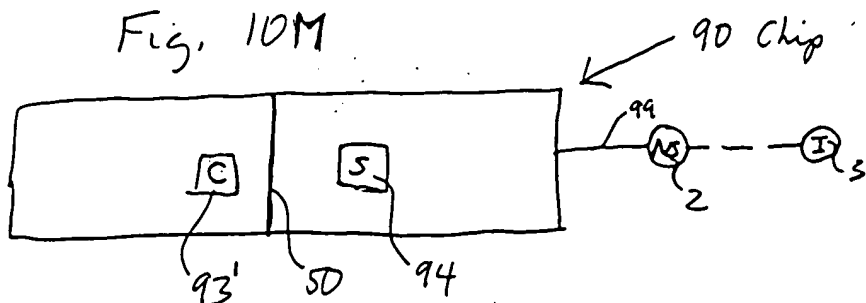


Fig. 10M



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Fig. 10"0"

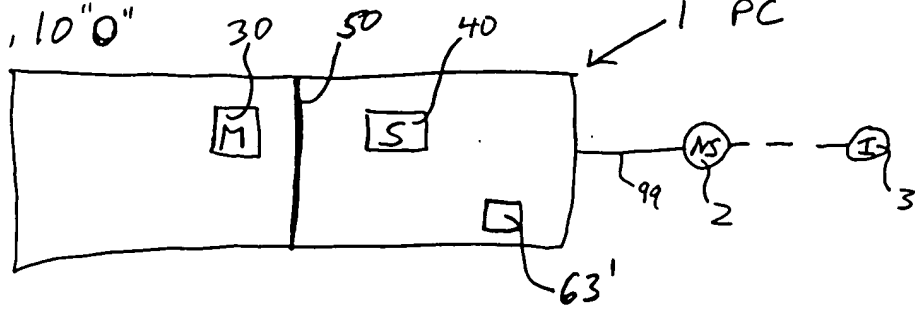
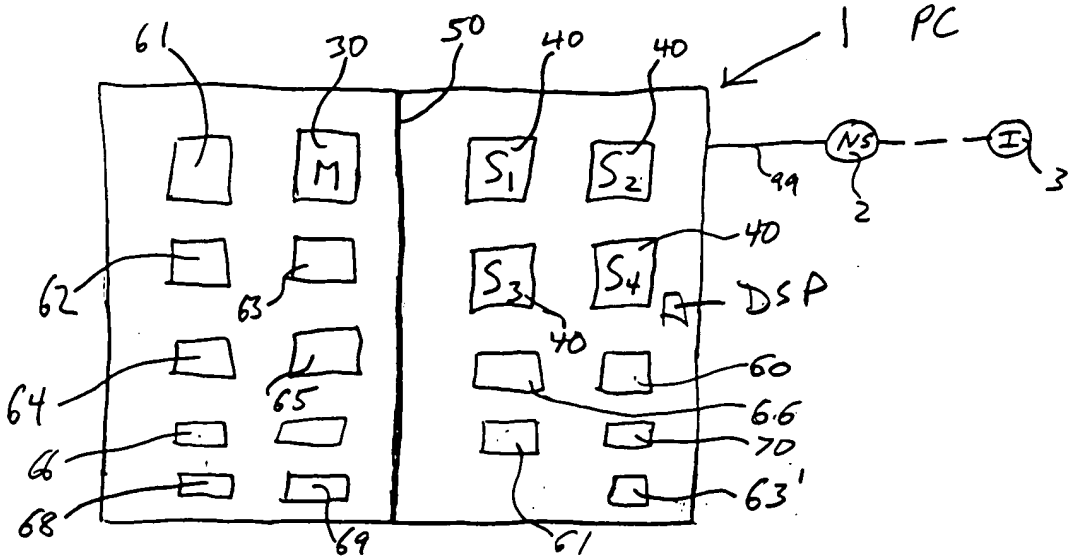


Fig. 10N



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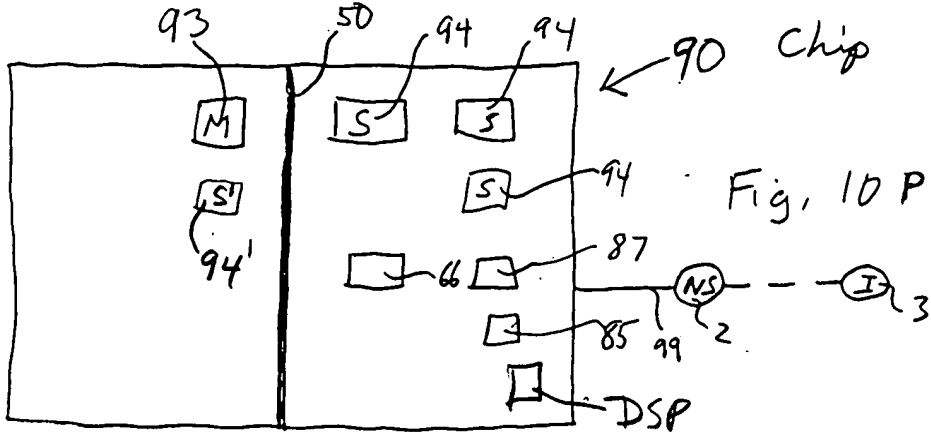
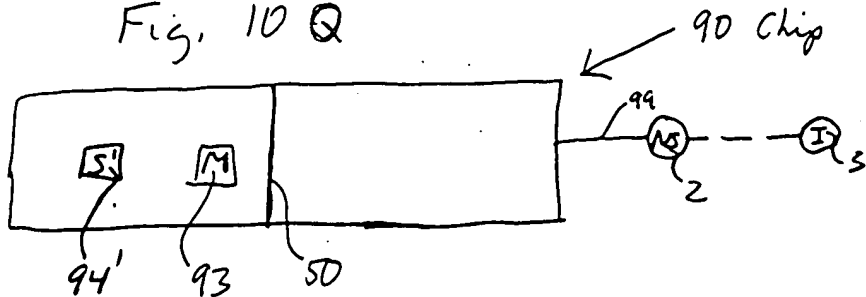
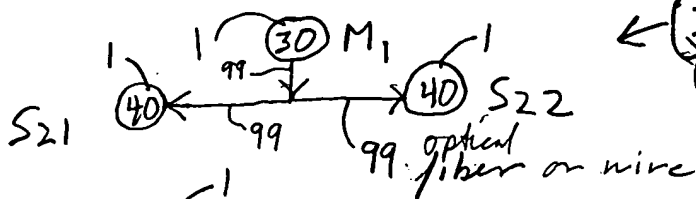


Fig. 10 Q



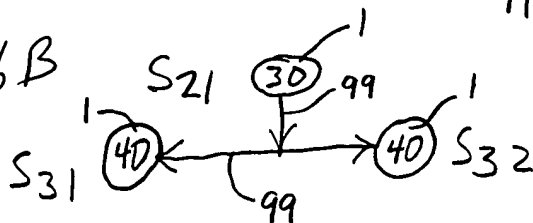
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Fig. 16A



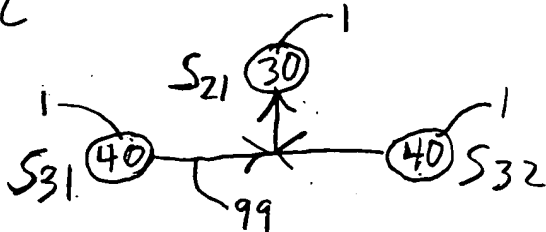
(Part of Internet or Intranet or other net)

Fig. 16B



Figs 16A-Q & 16V-AA:

Fig. 16C



30 indicates either master PC 1 or master microprocessor 30 chip within a PC 1.

Like vice, 40 indicates either a slave PC 1 or a slave microprocessor 40 chip within a PC 1.

Either microprocessor 30 or microprocessor 40 can be a microprocessor 90, a PC 1 or a microchip

Fig. 16D



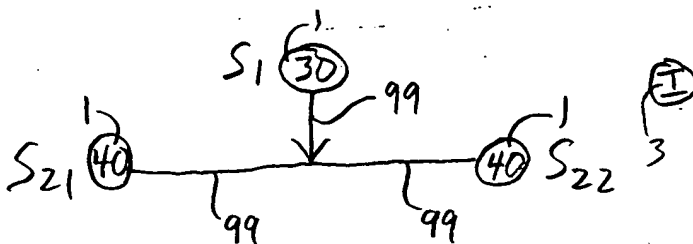
Note 100: mix of 100 & 99

Fig. 16G



Master PC offloads operation to Slave PC 1, which functions as M1

Fig. 16H



Unavailable S21 offloads results of S31 & S32 to S21', which takes over

Fig. 16I

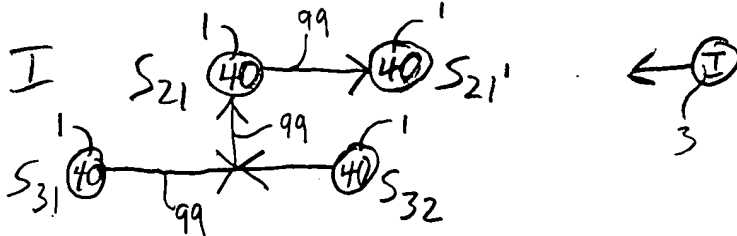
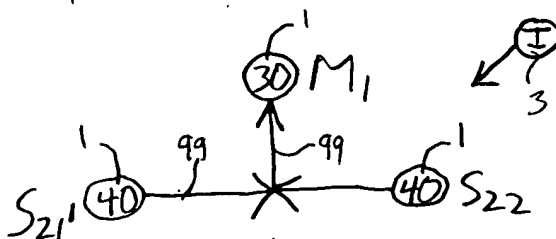


Fig. 16J



Like Fig. 16D S21' replaces S21

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Fig. 16 E

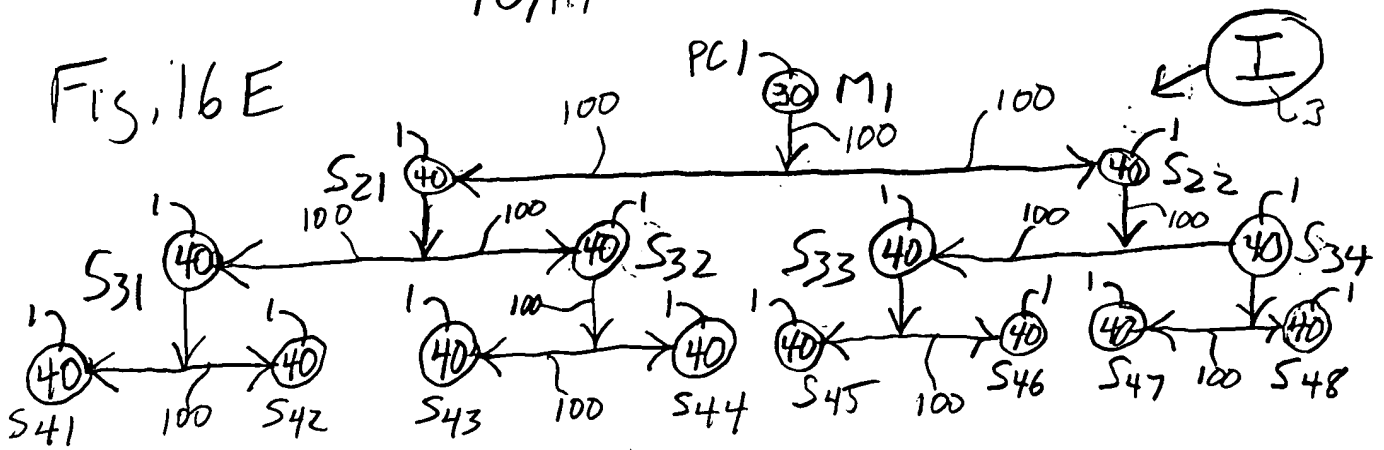


Fig. 16 F

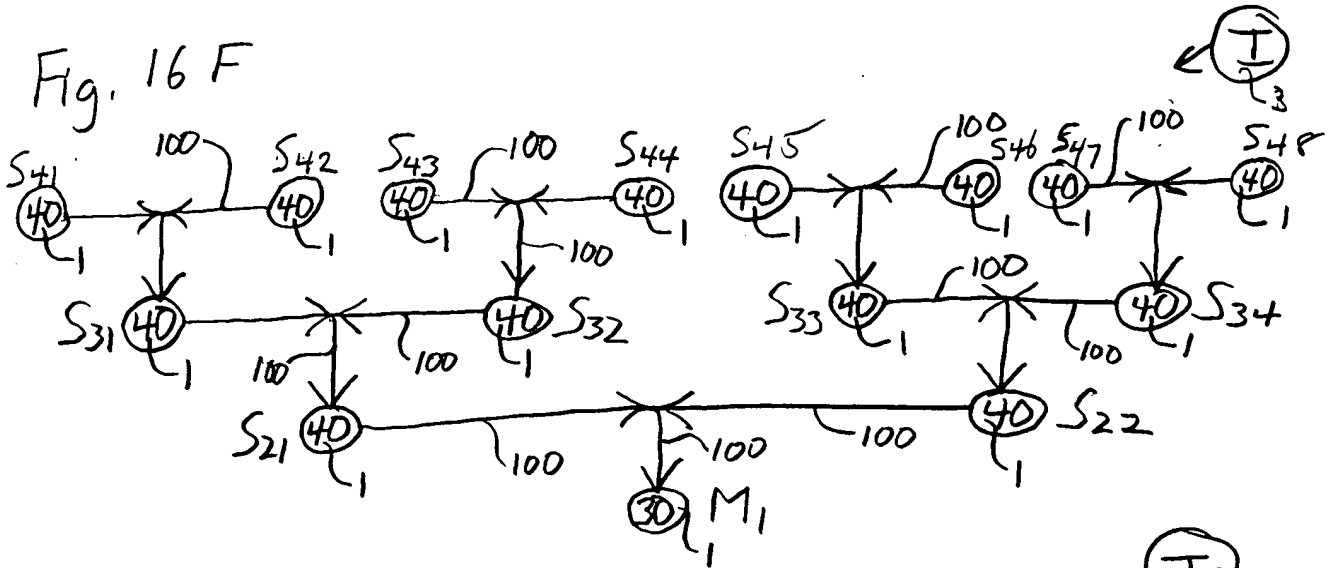
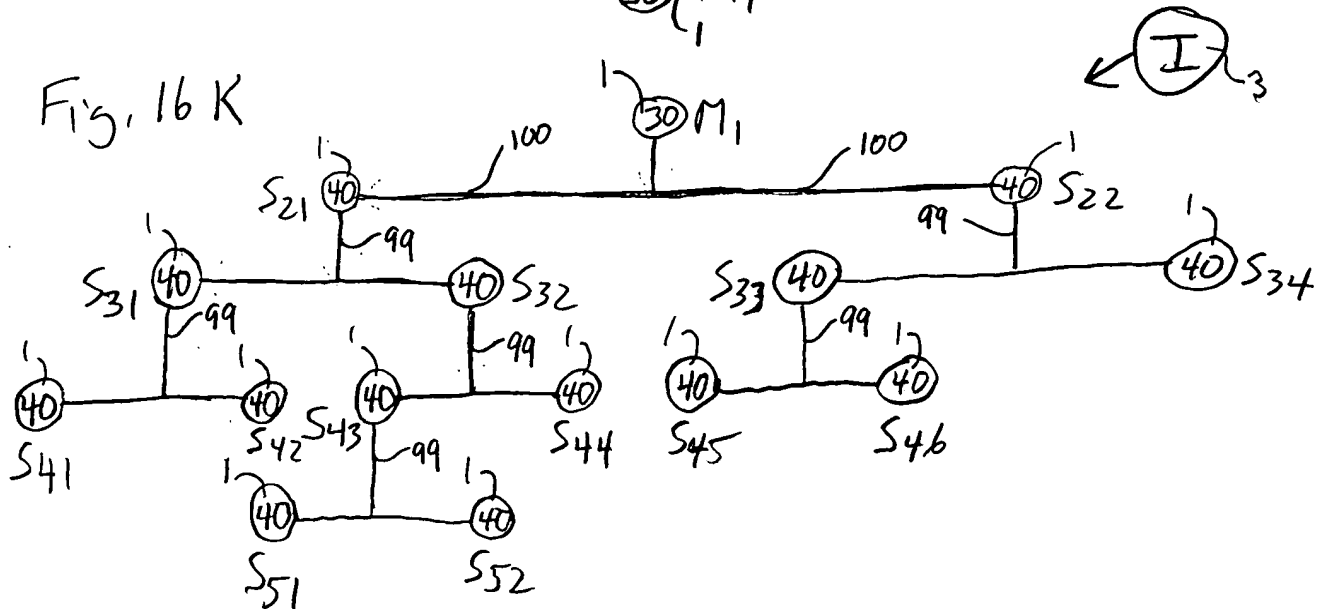


Fig. 16 K



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Fig. 16L

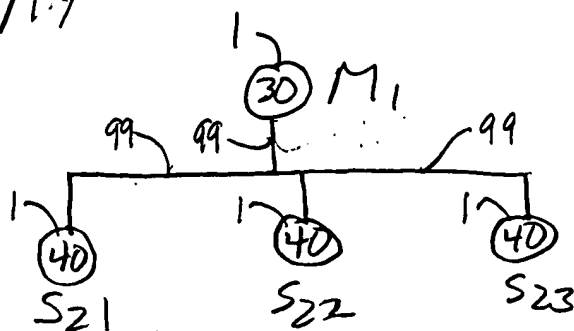


Fig. 16M

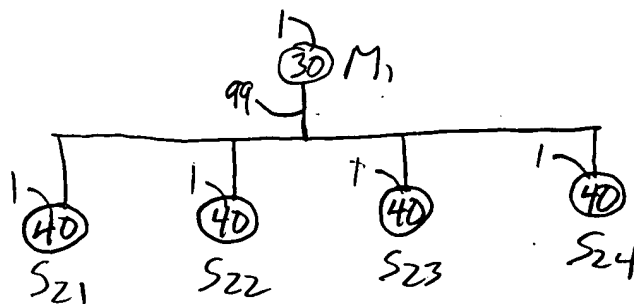


Fig. 16N

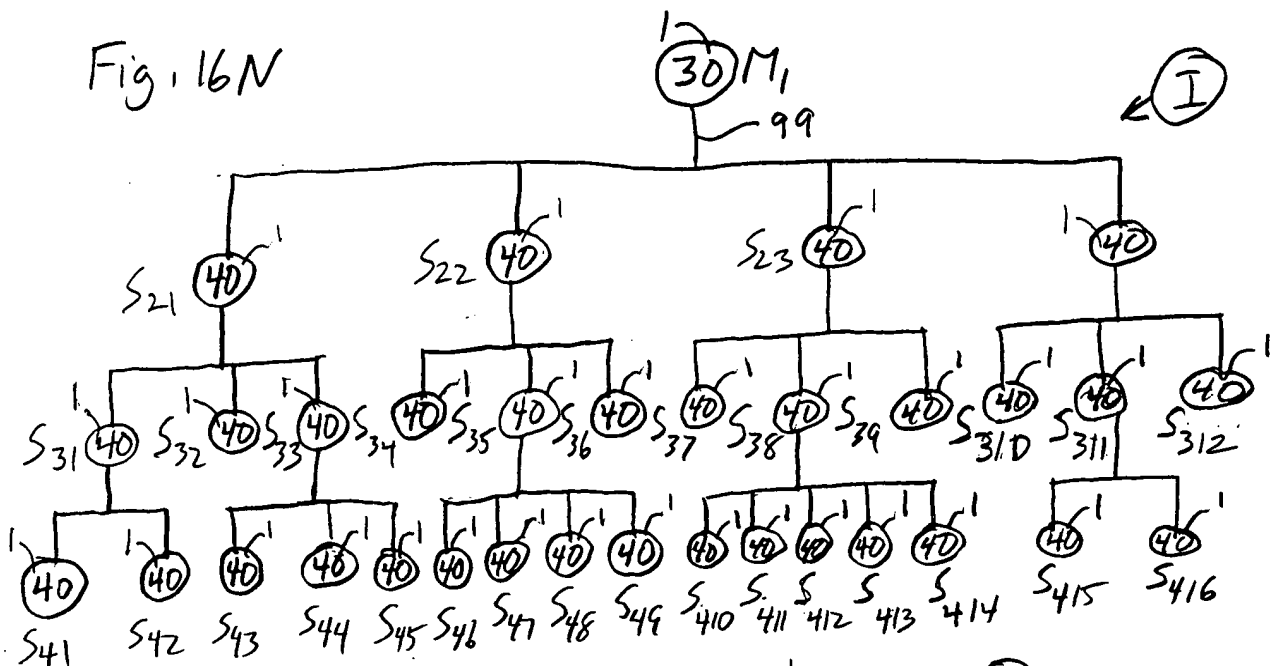


Fig. 16O

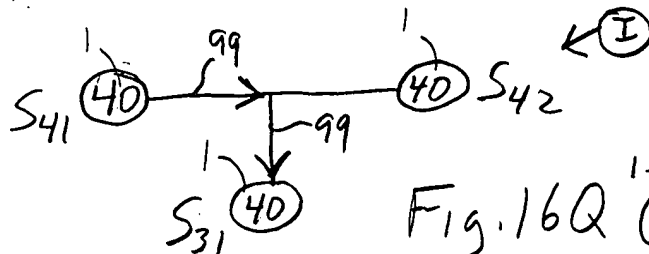


Fig. 16Q

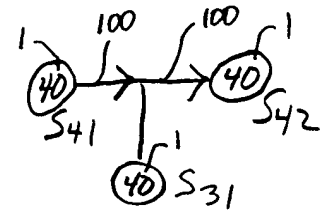
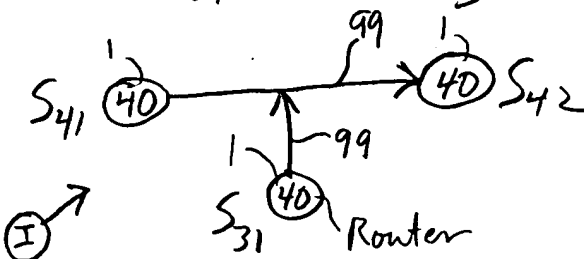


Fig. 16P



Figs. 16O-Q
are sections
of Fig 16 F
Net (left upper)

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like
Fig 10A
& 10B

Fig. 16X

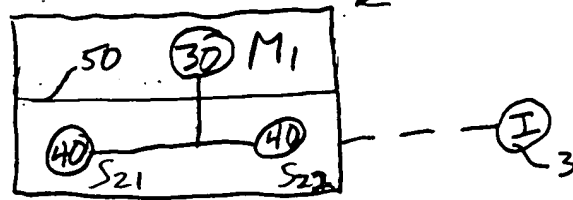


Fig. 16Y

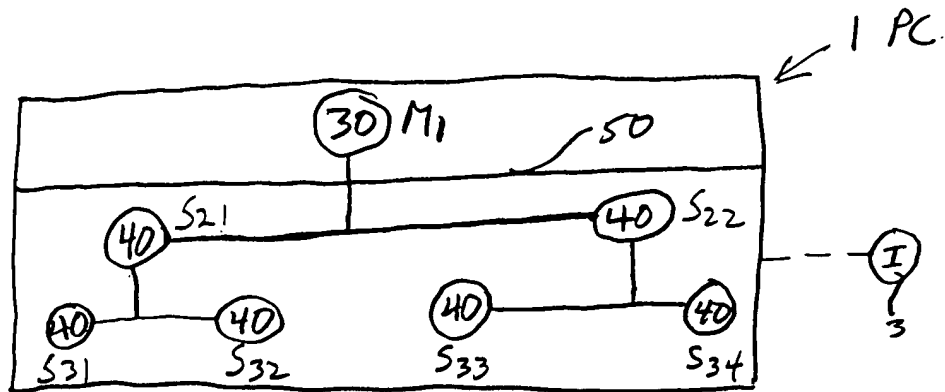


Fig. 16Z

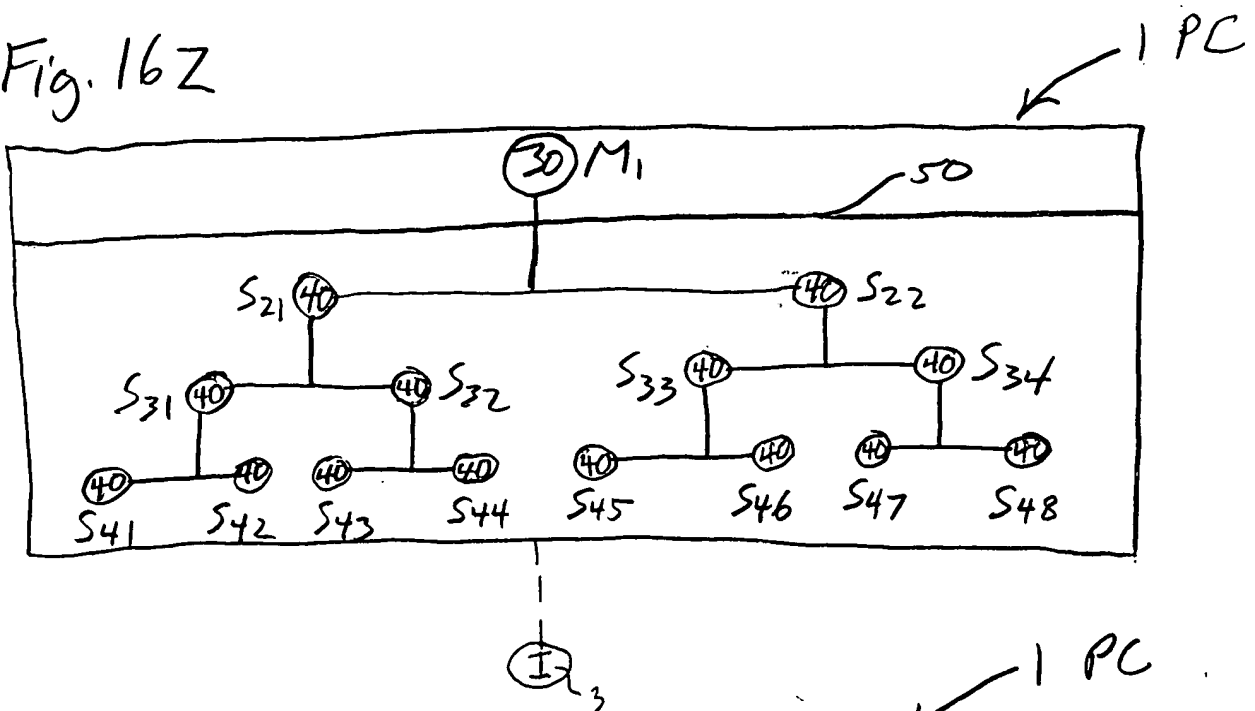
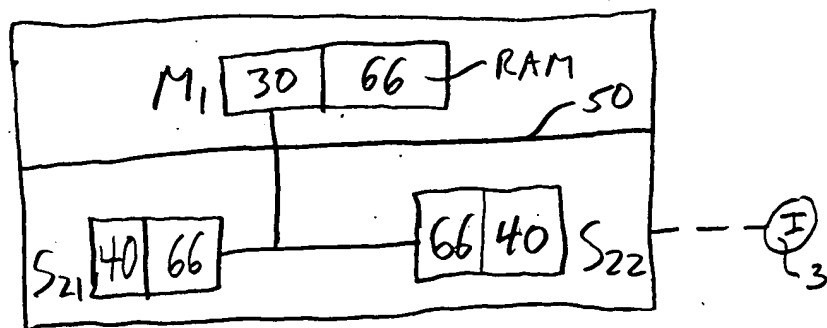
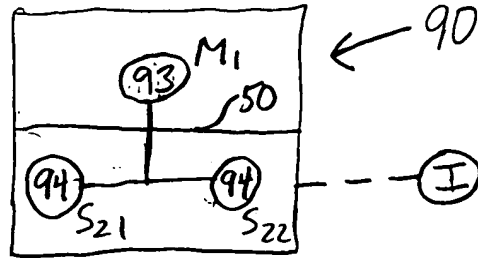


Fig. 16AA



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Fig. 16R



Microchip

Like Fig. 10C:
"Personal Computer
on a chip"
(Figs. 16R-16U)

Fig. 16S

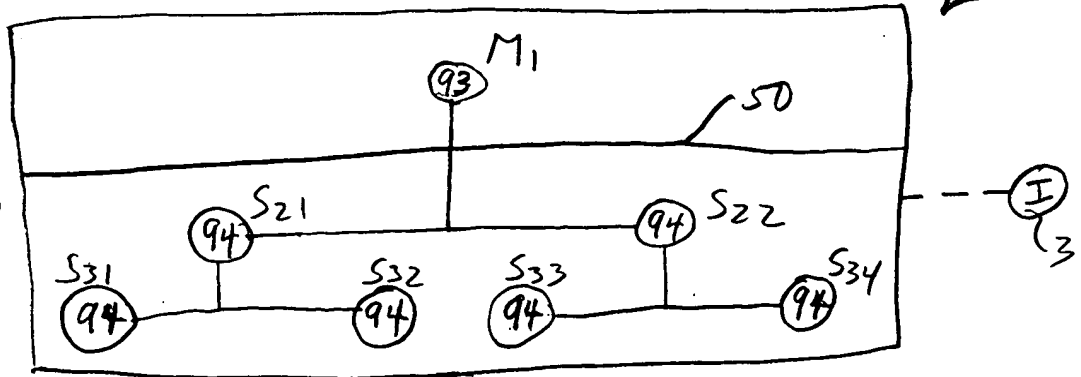


Fig. 16T

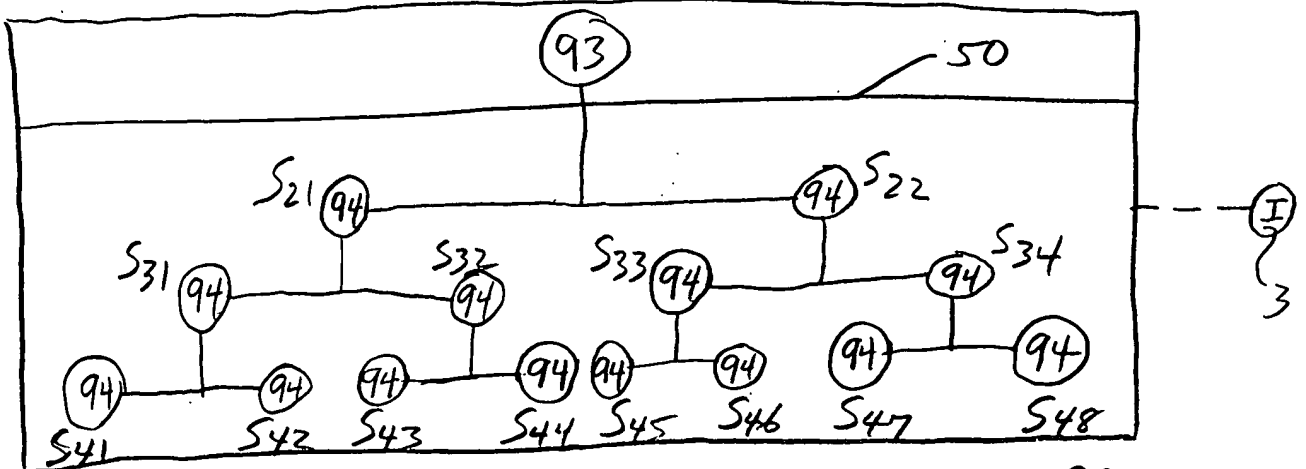
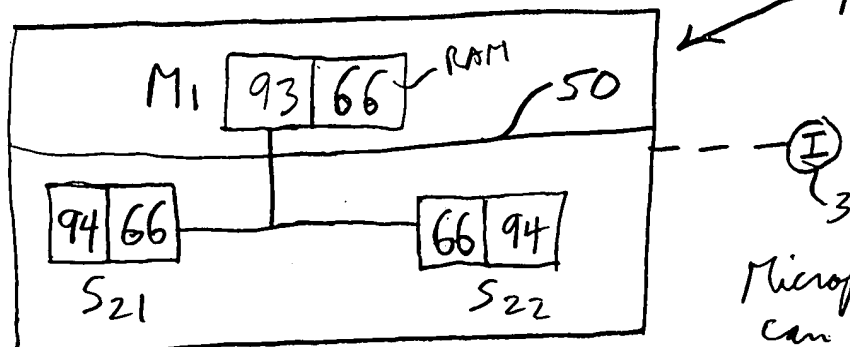


Fig. 16U



Microprocessors 90
can be entire
PCI on a single
microchip

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Fig. 16V

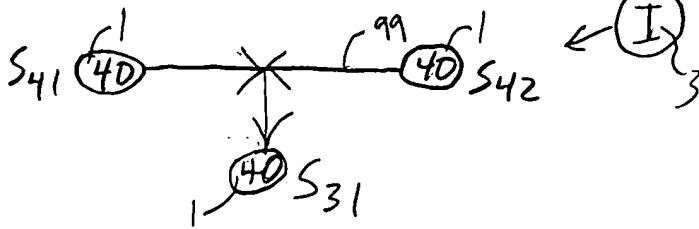


Fig. 16W-X
follows Fig.
16Q-Q &
are also sections
of Fig. 16F net

Fig. 16W

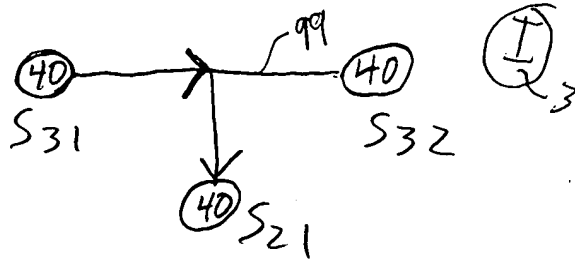


Fig. 17C

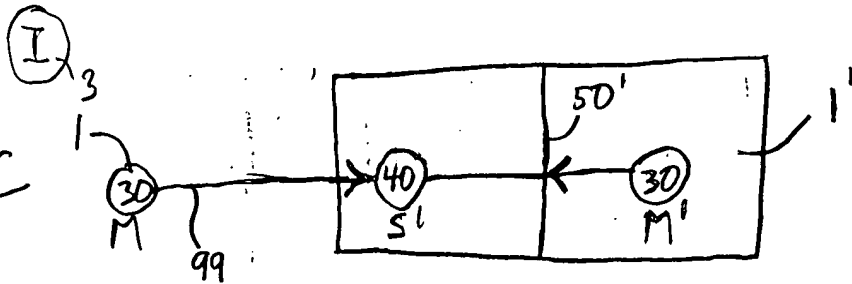


Fig. 17A

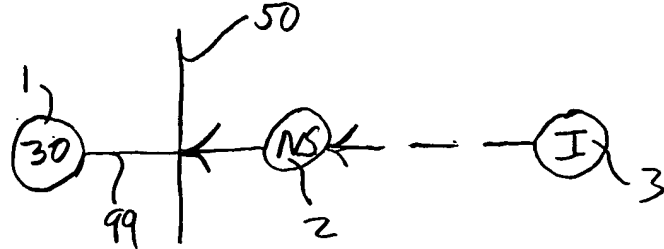


Fig. 17B

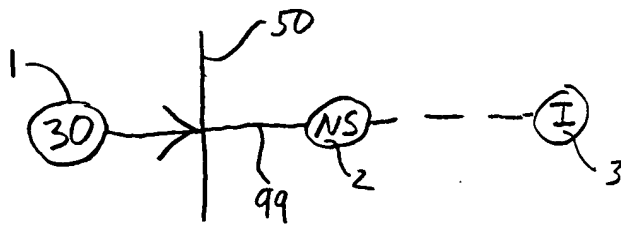
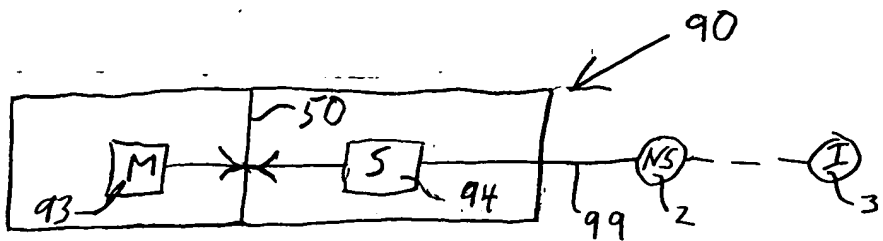


Fig. 17D



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Fig. 18A

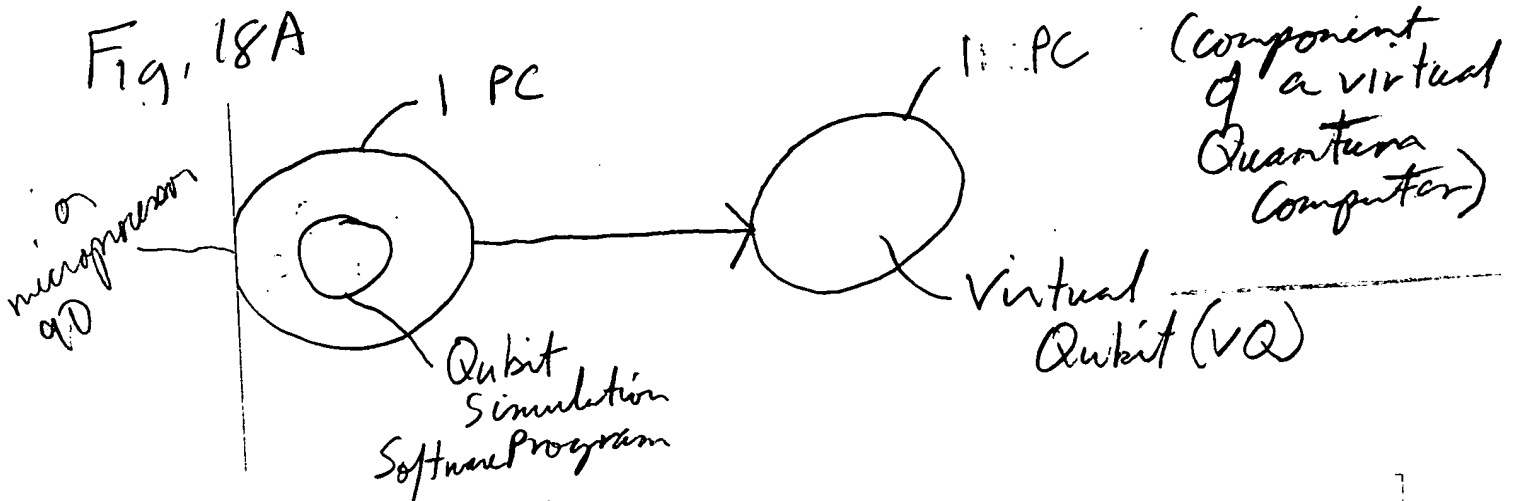


Fig. 18B

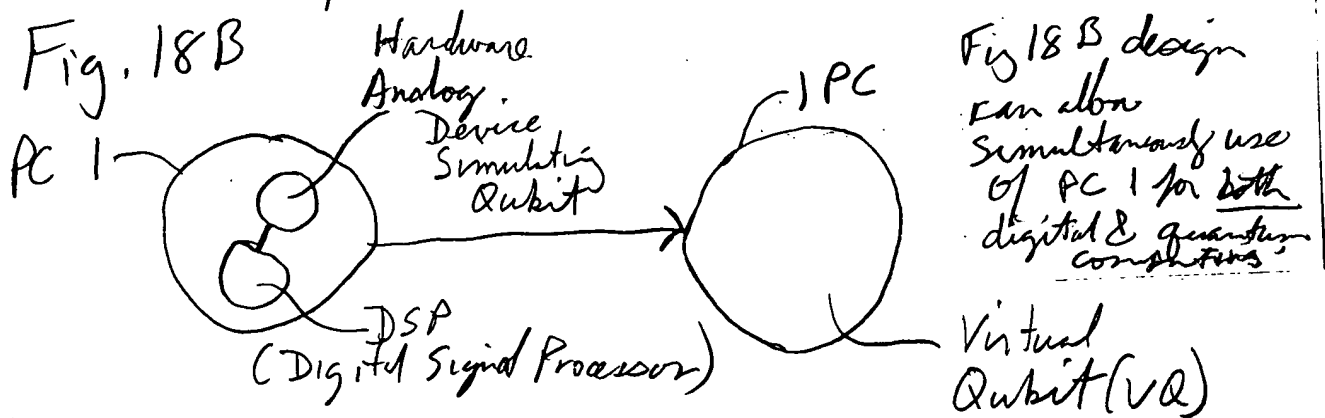


Fig. 18C

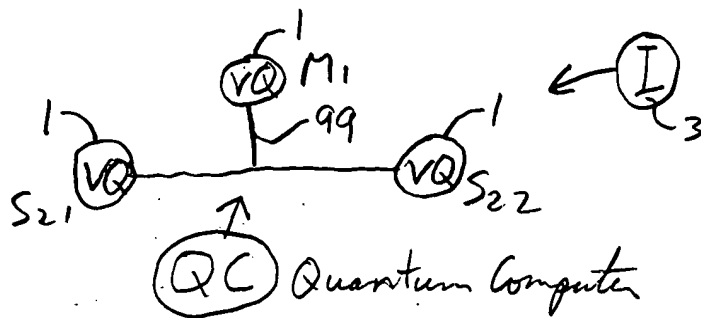


Fig. 18D

Like Fig. 13

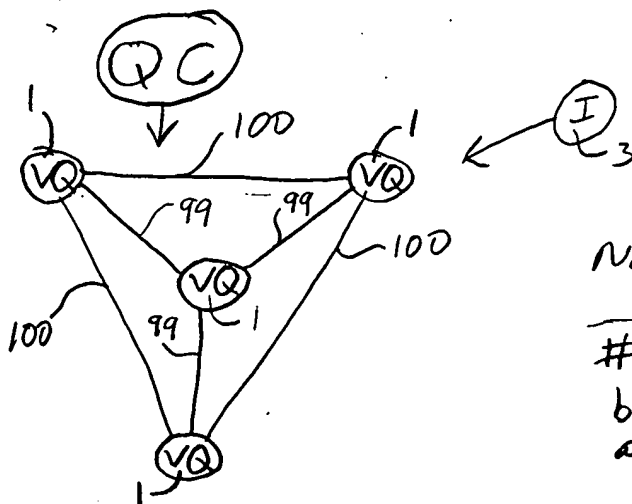


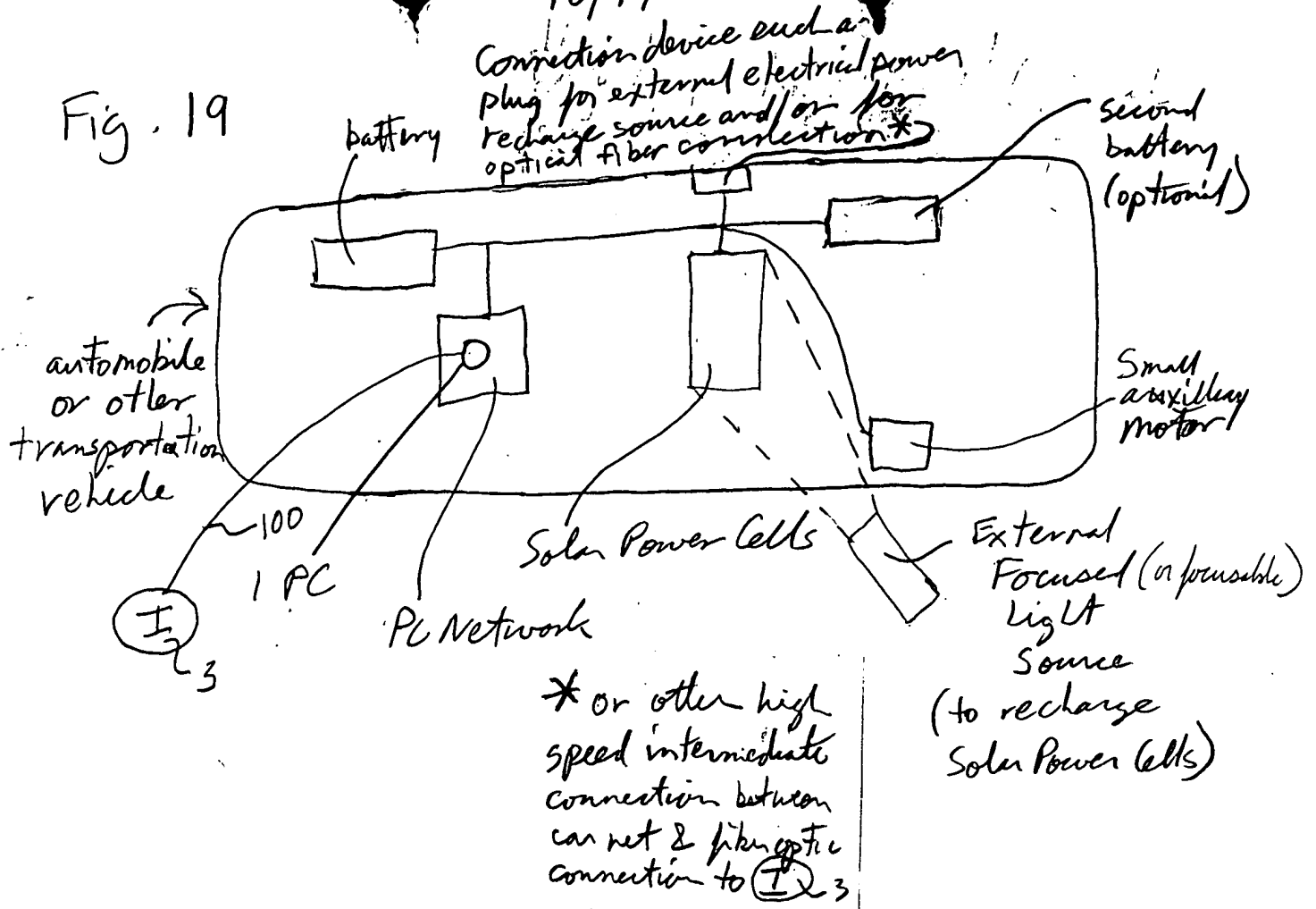
Fig. 18 like Fig. 16A & similarly VQ could be substituted for 30 & 40 in Figs. 16B-16Q & 16V-16AA and in earlier Figures

Note 99 & 100 mix

of VQ can be scaled to any size Quantum Computer QC

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Fig. 19



000055.059798

Fig. 20A

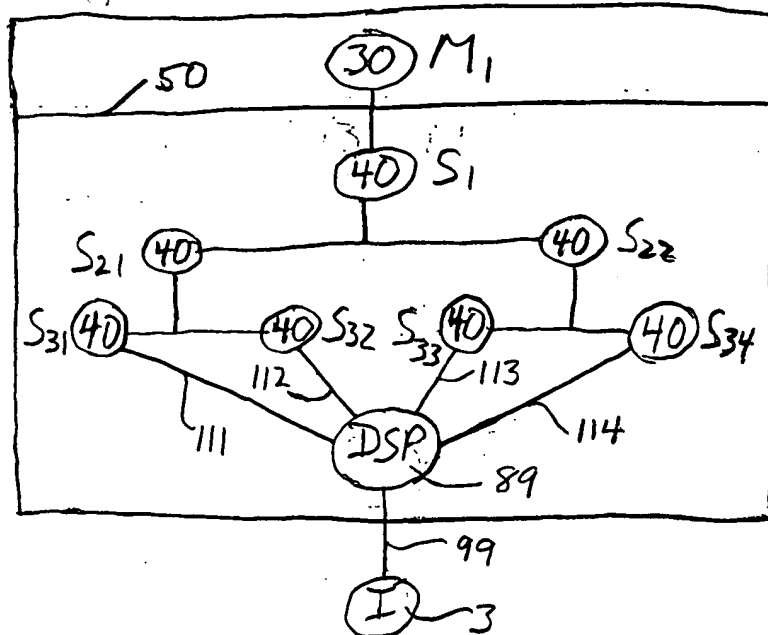


Fig. 20B

